

We Claim:

1. A RAKE receiver for receiving signals transmitted by different propagation paths of a transmission channel and spread-coded with chip sequences in frequency division duplex (FDD) and time division duplex (TDD) modes, the rake receiver comprising:

at least two RAKE fingers;

an equalizer connected to said RAKE fingers for equalization of the signals processed in individual ones of said RAKE fingers, using equalizer coefficients; and

means for calculating the equalizer coefficients for the FDD and TDD modes selectively, the equalizer coefficients for the TDD mode being calculated using a multiple subscriber method on a propagation path non-specific basis for each chip of the signals to be equalized, and being applied to the signals, said means for calculating being connected to said equalizer.

2. The RAKE receiver according to claim 1, wherein:

said rake fingers each have a unit for signal rate reduction disposed in a signal path upstream of said equalizer; and

said rake fingers each have a means for bridging said unit for signal rate reduction.

3. The RAKE receiver according to claim 2, wherein each of said rake fingers has despreading means for despreading the signals being processed in said RAKE fingers, said despreading means disposed in the signal path upstream of said unit for signal rate reduction in each of said RAKE fingers, said despreading means being deactivated in the TDD mode.

4. The RAKE receiver according to claim 3, wherein said despreading means is a multiplier.

5. The RAKE receiver according to claim 2, wherein said unit for signal rate reduction is an accumulator.

6. A RAKE receiver for receiving signals transmitted by different propagation paths of a transmission channel and spread-coded with chip sequences in frequency division duplex and time division duplex modes, the rake receiver comprising:

at least two RAKE fingers;

an equalizer connected to said RAKE fingers for equalization of the signals processed in individual ones of said RAKE fingers, using equalizer coefficients; and

a calculation unit for generating the equalizer coefficients for the FDD and TDD modes selectively, the equalizer coefficients for the TDD mode being calculated using a multiple subscriber method on a propagation path non-specific basis for each chip of the signals to be equalized, and being applied to the signals, said calculation unit connected to said equalizer.